

# Si APD

S10341 series

Low bias operation, for 800 nm band, small package

# Features

- Miniature and thin package: 1.8 × 3.1 × 1.0<sup>t</sup> mm
- Stable operation at low bias
- High-speed response
- High sensitivity
- → Low noise

# - Applications

- Optical rangefinder
- → Laser radar
- **FSO** (free space optics)

# Structure

Parameter	Symbol	S10341-02	S10341-05	Unit
Photosensitive area size*1	A	φ0.2	φ0.5	mm
Effective photosensitive area	-	0.03	0.19	mm <sup>2</sup>
Package	-	Pla	-	

\*1: Photosensitive area in which a typical gain can be obtained

# Absolute maximum ratings

Parameter	Symbol	Value	Unit
Operating temperature*2	Topr	-20 to +60	°C
Storage temperature* <sup>2</sup>	Tstg	-40 to +80	°C
Reverse current (DC)	Irmax	200	μA
Forward current	Ifmax	10	mA
Soldering conditions*3	Tsol	Peak temperature: 235 °C , twice	-

\*2: No dew condensation

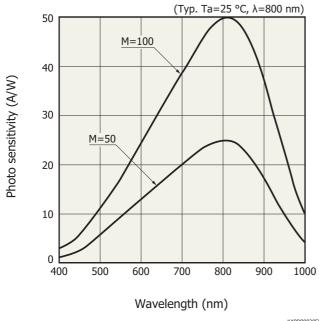
When there is a temperature difference between a product and the surrounding area in high humidity environment, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability. \*3: JEDEC level 5a

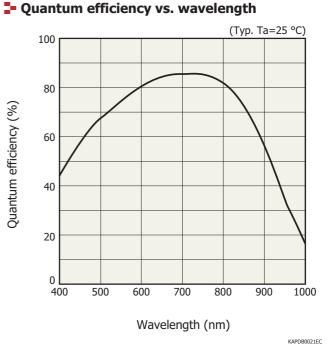
Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

# Electrical and optical characteristics (Ta=25 °C)

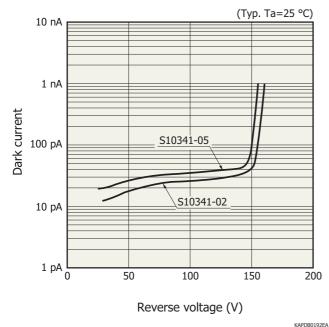
Daramatar	Cumbol	Condition	S10341-02		S10341-05		Unit		
Parameter	Symbol	Condition	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
Spectral response range	λ		400 to 1000 400 to 1000		00	nm			
Peak sensitivity wavelength	λр	M=100	-	800	-	-	800	-	nm
Photo sensitivity	S	λ=800 nm, M=1	-	0.5	-	-	0.5	-	A/W
Quantum efficiency	QE	λ=800 nm, M=1	-	75	-	-	75	-	%
Breakdown voltage	VBR	ID=100 μA	-	150	200	-	150	200	V
Temperature coefficient of VBR	-		-	0.65	-	-	0.65	-	V/°C
Dark current	ID	M=100	-	50	500	-	100	1000	pА
Cut-off frequency	fc	M=100, RL=50 Ω λ=800 nm, -3 dB	-	1000	-	-	900	-	MHz
Terminal capacitance	Ct	M=100, f=1 MHz	-	1	-	-	2	-	pF
Excess noise figure	х	M=100	-	0.3	-	-	0.3	-	-
Gain	М	λ=800 nm	-	100	-	-	100	-	-

# Spectral response



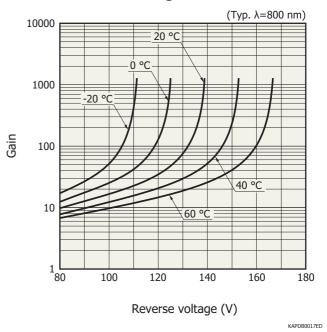


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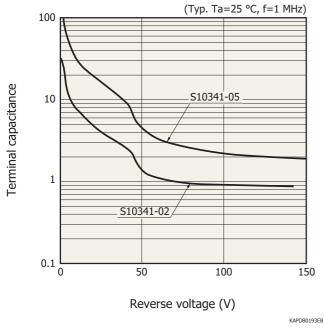
Dark current vs. reverse voltage

Gain vs. reverse voltage



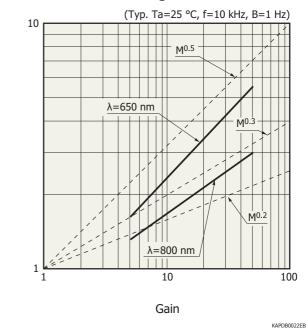


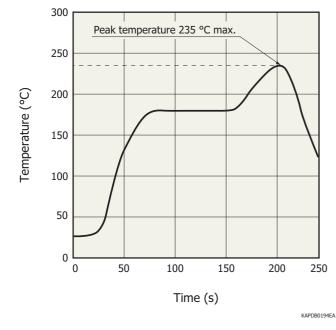
Excess noise factor



# Terminal capacitance vs. reverse voltage





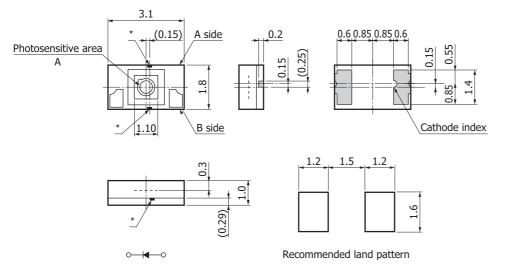


# - Recommended solder reflow condition

- After unpacking, store this device in an environment at a temperature of 30 °C and a humidity below 60%, and perform reflow soldering on this device within 24 hours.
- Thermal stress applied to the device during reflow soldering differs depending on the PC boards and reflow oven being used.
- When setting the reflow conditions, make sure that the reflow soldering process does not degrade device reliability.



Dimensional outline (unit: mm)



Tolerance unless otherwise noted:  $\pm 0.2$ Photosensitive area position accuracy: X, Y $\leq \pm 0.3$ Values in parentheses indicate reference value. \* Package side

Wiring is exposed on A and B sides. Do not allow any conductor to make contact with the package sides to avoid shorting.

Electrodes

Type no.	А
S10341-02	ф0.2
S10341-05	φ0.5

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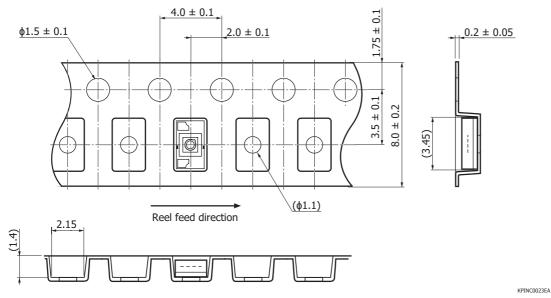


# Standard packing specifications

Reel (conforms to JEITA ET-7200)

Dimensions	Hub diameter	Tape width	Material	Electrostatic characteristics
180 mm	60 mm	8 mm	PS	Conductive

## Embossed tape (unit: mm, material: PS, conductive)



Packing quantity 1000 pcs/reel

Reel and desiccant in moisture-proof packaging (vacuum-sealed)

# Related information

www.hamamatsu.com/sp/ssd/doc\_en.html

- Precautions
- · Disclaimer
- · Surface mount type products
- Technical information
- · Si APD



Packing type

Information described in this material is current as of July 2017.

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